



PNP Silicon Planar Epitaxial Transistors

FEATURES

Power dissipation

P_{CM} : 0.625 W (Tamb=25°C)

Collector current

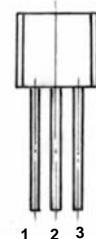
I_{CM} : -0.1 A

Collector-base voltage

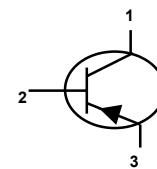
V_{CBO} : BC556 -80 V

BC557 -50 V

BC558 -30 V



1. COLLECTOR
2. BASE
3. Emitter



Operating and storage junction temperature range

TO-92

T_J, T_{stg} : -55°C to +150°C

Electrical Characteristics (Ta=25 °C unless otherwise specified)

		SPEC	min	max	unit
Collector-base breakdown voltage	BC556		-80		
	BC557	V_{CBO}	$I_C = -100\mu A, I_E = 0$	-50	V
	BC558		-30		
Collector-emitter breakdown voltage	BC556		-65		
	BC557	V_{CEO}	$I_C = -2mA, I_B = 0$	-45	V
	BC558		-30		
Emitter-base breakdown voltage		V_{EBO}	$I_E = -100\mu A, I_C = 0$	-6	V
Collector cut-off current	BC556		$V_{CB} = -70V, I_E = 0$		
	BC557	I_{CBO}	$V_{CB} = -45V, I_E = 0$	-0.1	μA
	BC558		$V_{CB} = -25V, I_E = 0$		
Collector cut-off current	BC556		$V_{CE} = -60V, I_B = 0$		
	BC557	I_{CEO}	$V_{CE} = -40V, I_B = 0$	-0.1	μA
	BC558		$V_{CE} = -25V, I_B = 0$		
Emitter cut-off current	BC556				
	BC557	I_{EBO}	$V_{EB} = -5V, I_C = 0$	-0.1	μA
	BC558				
DC current gain	BC556		120	500	
	BC557		120	800	
	BC558		120	800	
	BC557A	$h_{FE(1)}$	120	220	
	BC556B/BC557B/BC558B		180	460	
	BC557C		420	800	
Collector-emitter saturation voltage		$V_{CE(sat)}$	$I_C = -100mA, I_B = -5mA$	-0.3	V
Base-emitter saturation voltage		$V_{BE(sat)}$	$I_C = -100mA, I_B = -5mA$	-1	V
Transition frequency		f_T	$V_{CE} = -5V, I_C = -10mA$ $f = 100MHz$	150	MHz