

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE ( $\pi$ -MOS V)

## 2SK2915

HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS

CHOPPER REGULATOR, DC-DC CONVERTER AND MOTOR DRIVE APPLICATIONS

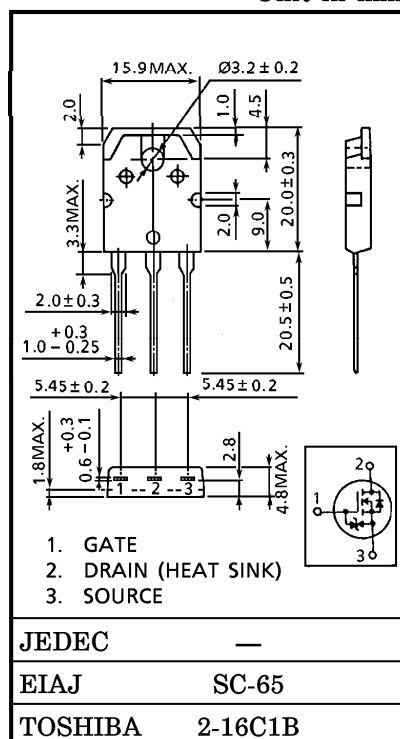
INDUSTRIAL APPLICATIONS

Unit in mm

- Low Drain-Source ON Resistance :  $R_{DS(ON)} = 0.31\Omega$  (Typ.)
- High Forward Transfer Admittance :  $|Y_{fs}| = 15S$  (Typ.)
- Low Leakage Current :  $I_{DSS} = 100\mu A$  (Max.) ( $V_{DS} = 600V$ )
- Enhancement-Mode :  $V_{th} = 2.0 \sim 4.0V$  ( $V_{DS} = 10V$ ,  $I_D = 1mA$ )

MAXIMUM RATINGS ( $T_a = 25^\circ C$ )

| CHARACTERISTIC                                 | SYMBOL    | RATING         | UNIT       |
|------------------------------------------------|-----------|----------------|------------|
| Drain-Source Voltage                           | $V_{DSS}$ | 600            | V          |
| Drain-Gate Voltage ( $R_{GS} = 20k\Omega$ )    | $V_{DGR}$ | 600            | V          |
| Gate-Source Voltage                            | $V_{GSS}$ | $\pm 30$       | V          |
| Drain Current                                  | DC        | $I_D$          | A          |
|                                                | Pulse     | $I_{DP}$       |            |
| Drain Power Dissipation ( $T_c = 25^\circ C$ ) | $P_D$     | 150            | W          |
| Single Pulse Avalanche Energy**                | $E_{AS}$  | 1026           | mJ         |
| Avalanche Current                              | $I_{AR}$  | 16             | A          |
| Repetitive Avalanche Energy*                   | $E_{AR}$  | 15             | mJ         |
| Channel Temperature                            | $T_{ch}$  | 150            | $^\circ C$ |
| Storage Temperature Range                      | $T_{stg}$ | $-55 \sim 150$ | $^\circ C$ |



Weight : 4.6g (Typ.)

## THERMAL CHARACTERISTICS

| CHARACTERISTIC                         | SYMBOL         | MAX.  | UNIT         |
|----------------------------------------|----------------|-------|--------------|
| Thermal Resistance, Channel to Case    | $R_{th(ch-c)}$ | 0.833 | $^\circ C/W$ |
| Thermal Resistance, Channel to Ambient | $R_{th(ch-a)}$ | 50    | $^\circ C/W$ |

Note ;

\* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

\*\*  $V_{DD} = 90V$ , Starting  $T_{ch} = 25^\circ C$ ,  $L = 7.01mH$ ,  $R_G = 25\Omega$ ,  $I_{AR} = 16A$ **This transistor is an electrostatic sensitive device.****Please handle with caution.**

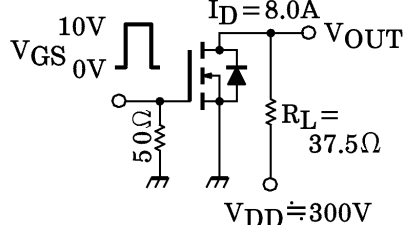
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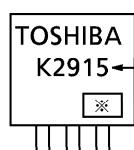
## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

| CHARACTERISTIC                                  |               | SYMBOL        | TEST CONDITION                                                                                                                                                          | MIN.     | TYP. | MAX.     | UNIT     |
|-------------------------------------------------|---------------|---------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------|------|----------|----------|
| Gate Leakage Current                            |               | $I_{GSS}$     | $V_{GS} = \pm 25V, V_{DS} = 0V$                                                                                                                                         | —        | —    | $\pm 10$ | $\mu A$  |
| Gate-Source Breakdown Voltage                   |               | $V_{(BR)GSS}$ | $I_G = \pm 10\mu A, V_{DS} = 0V$                                                                                                                                        | $\pm 30$ | —    | —        | V        |
| Drain Cut-off Current                           |               | $I_{DSS}$     | $V_{DS} = 600V, V_{GS} = 0V$                                                                                                                                            | —        | —    | 100      | $\mu A$  |
| Drain-Source Breakdown Voltage                  |               | $V_{(BR)DSS}$ | $I_D = 10mA, V_{GS} = 0V$                                                                                                                                               | 600      | —    | —        | V        |
| Gate Threshold Voltage                          |               | $V_{th}$      | $V_{DS} = 10V, I_D = 1mA$                                                                                                                                               | 2.0      | —    | 4.0      | V        |
| Drain-Source ON Resistance                      |               | $R_{DS(ON)}$  | $V_{GS} = 10V, I_D = 8.0A$                                                                                                                                              | —        | 0.31 | 0.4      | $\Omega$ |
| Forward Transfer Admittance                     |               | $ Y_{fs} $    | $V_{DS} = 10V, I_D = 8.0A$                                                                                                                                              | 8.0      | 15.0 | —        | S        |
| Input Capacitance                               |               | $C_{iss}$     | $V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$                                                                                                                                   | —        | 3520 | —        | pF       |
| Reverse Transfer Capacitance                    |               | $C_{rss}$     |                                                                                                                                                                         | —        | 20   | —        |          |
| Output Capacitance                              |               | $C_{oss}$     |                                                                                                                                                                         | —        | 300  | —        |          |
| Switching Time                                  | Rise Time     | $t_r$         |  <p><math>V_{GS} = 10V, I_D = 8.0A, R_L = 37.5\Omega, V_{DD} \approx 300V</math></p> | —        | 50   | —        | ns       |
|                                                 | Turn-on Time  | $t_{on}$      |                                                                                                                                                                         | —        | 100  | —        |          |
|                                                 | Fall Time     | $t_f$         |                                                                                                                                                                         | —        | 60   | —        |          |
|                                                 | Turn-off Time | $t_{off}$     |                                                                                                                                                                         | —        | 325  | —        |          |
| Total Gate Charge (Gate-Source Plus Gate-Drain) |               | $Q_g$         | $V_{DD} \approx 400V, V_{GS} = 10V, I_D = 16A$                                                                                                                          | —        | 80   | —        | nC       |
| Gate-Source Charge                              |               | $Q_{gs}$      |                                                                                                                                                                         | —        | 48   | —        |          |
| Gate-Drain ("Miller") Charge                    |               | $Q_{gd}$      |                                                                                                                                                                         | —        | 32   | —        |          |

## SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

| CHARACTERISTIC                   | SYMBOL    | TEST CONDITION                | MIN. | TYP. | MAX. | UNIT    |
|----------------------------------|-----------|-------------------------------|------|------|------|---------|
| Continuous Drain Reverse Current | $I_{DR}$  | —                             | —    | —    | 16   | A       |
| Pulse Drain Reverse Current      | $I_{DRP}$ | —                             | —    | —    | 64   | A       |
| Diode Forward Voltage            | $V_{DSF}$ | $I_{DR} = 16A, V_{GS} = 0V$   | —    | —    | -1.7 | V       |
| Reverse Recovery Time            | $t_{rr}$  | $I_{DR} = 16A, V_{GS} = 0V$   | —    | 620  | —    | ns      |
| Reverse Recovery Charge          | $Q_{rr}$  | $dI_{DR} / dt = 100A / \mu s$ | —    | 7.5  | —    | $\mu C$ |

## MARKING



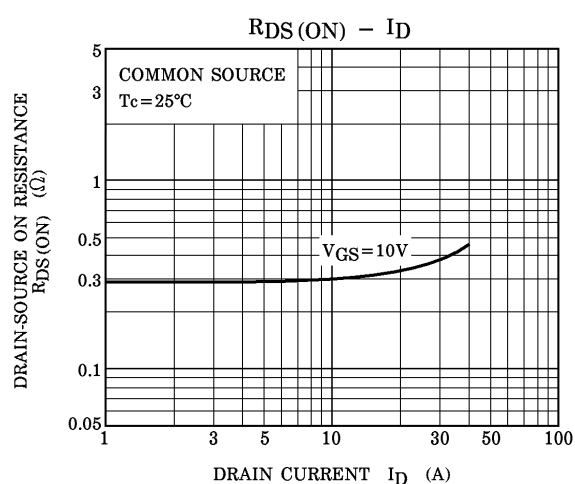
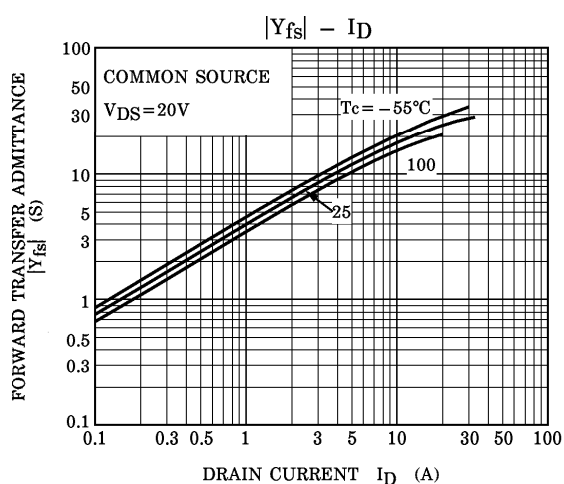
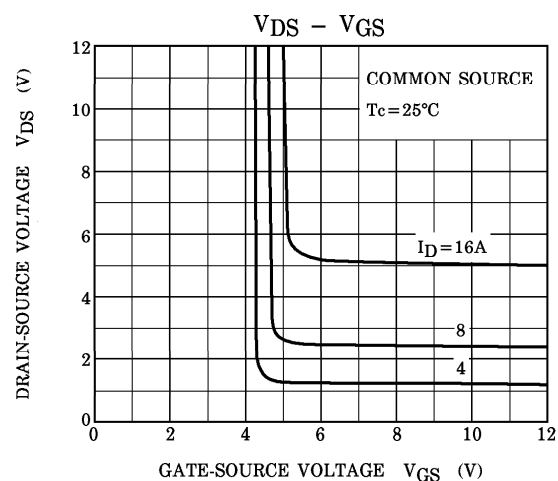
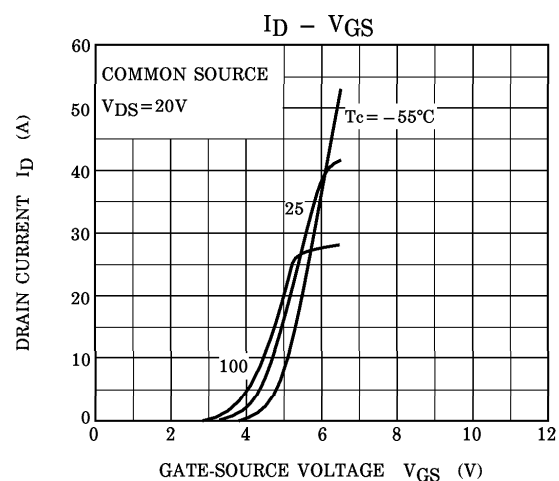
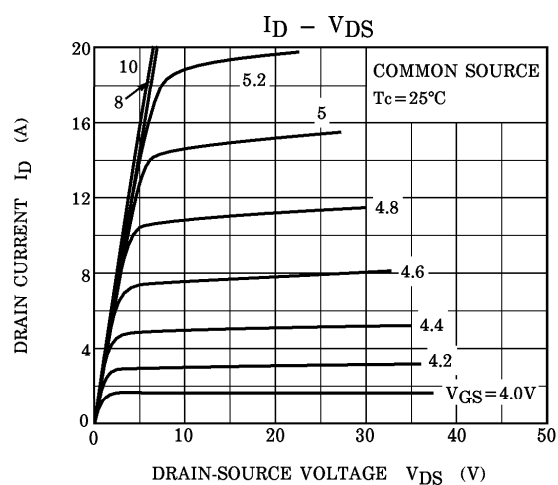
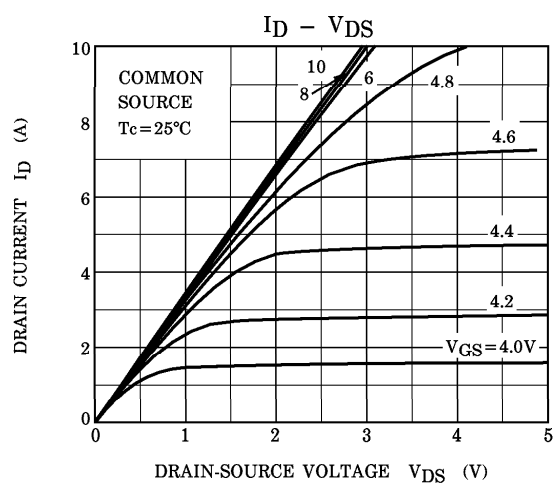
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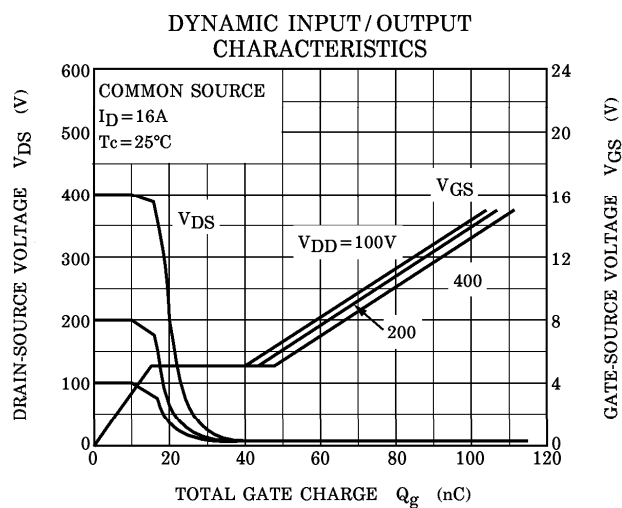
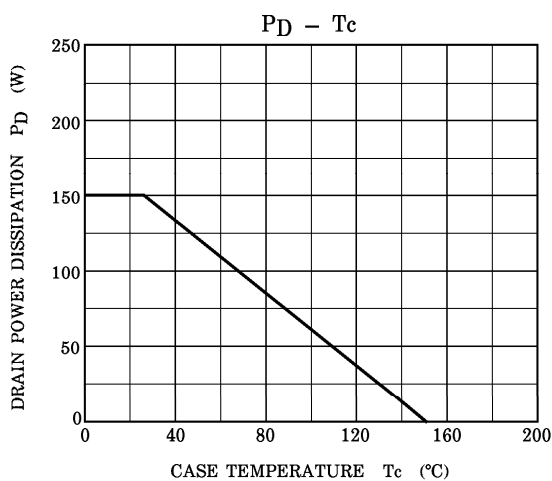
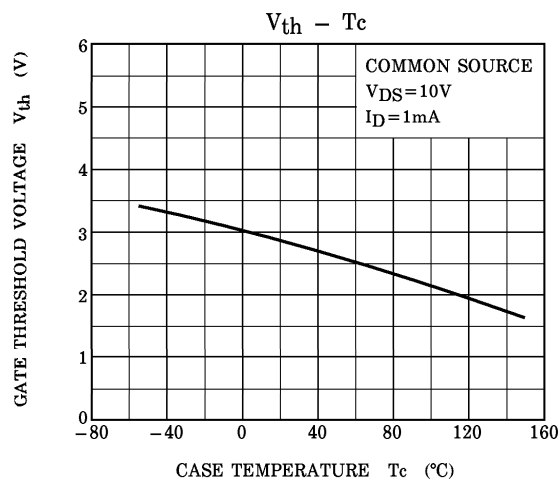
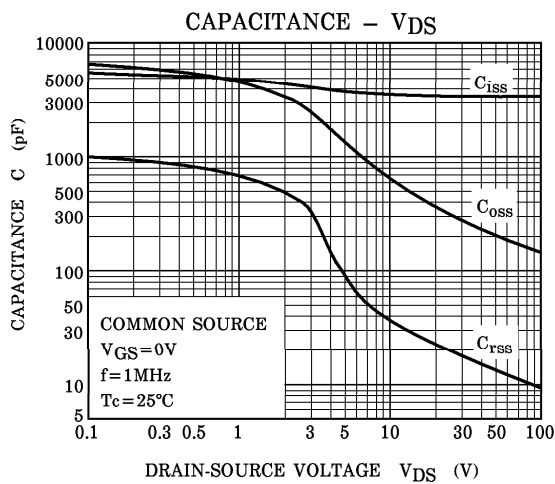
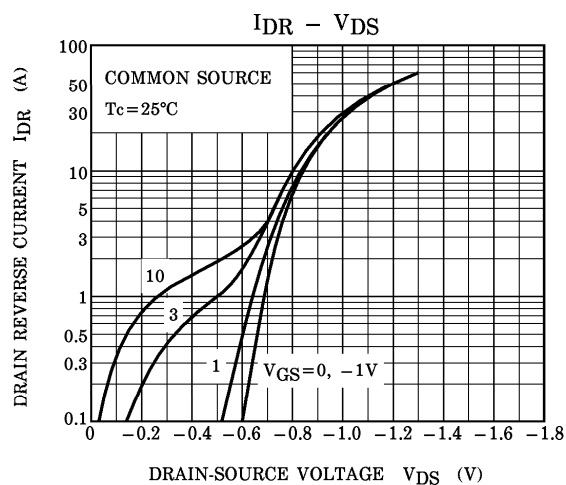
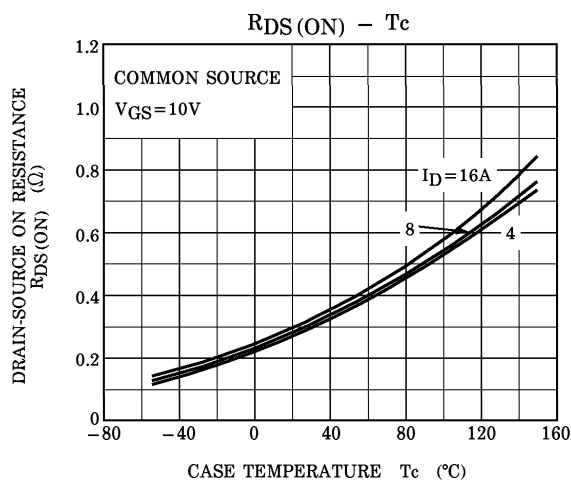
※ Lot Number

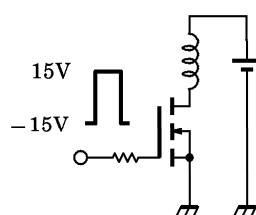
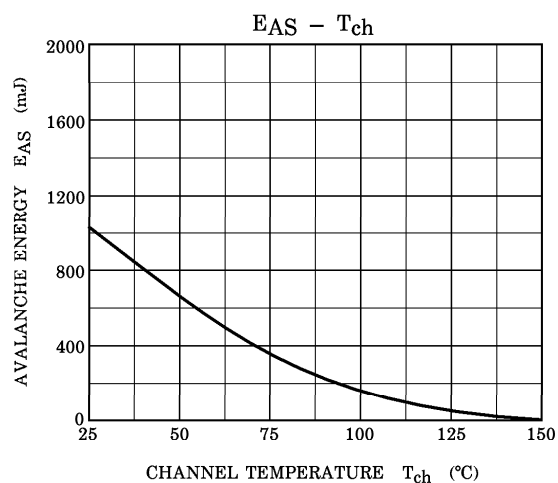
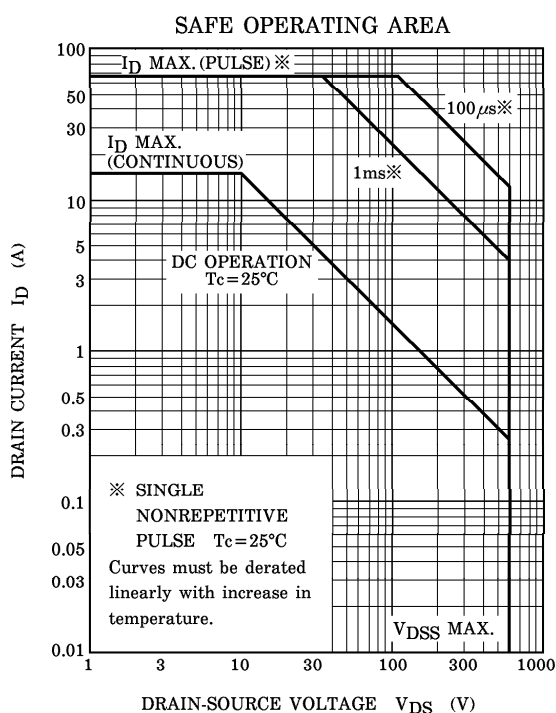
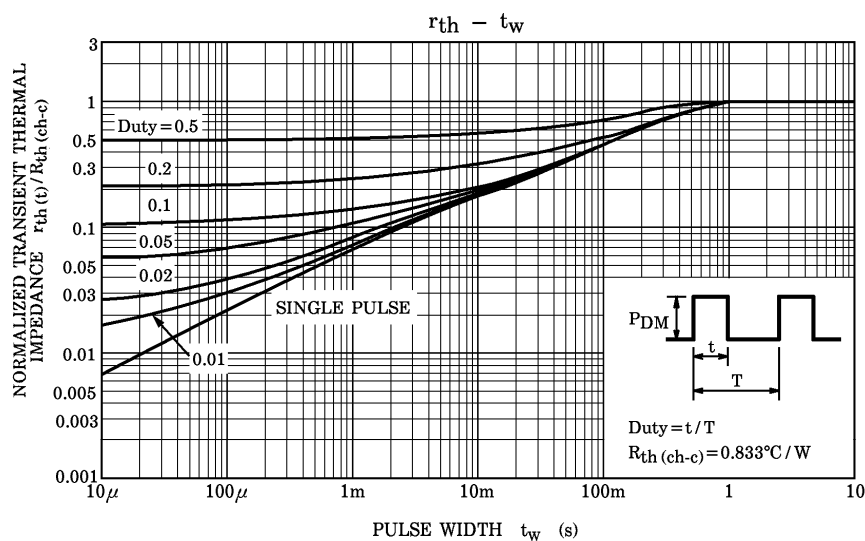


Month (Starting from Alphabet A)

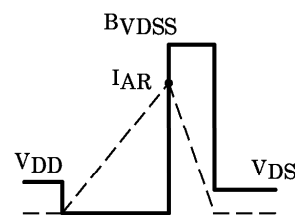
Year (Last Number of the Christian Era)







TEST CIRCUIT



WAVE FORM

Peak  $I_{AR} = 16A$ ,  $R_G = 25\Omega$   $E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$   
 $V_{DD} = 90V$ ,  $L = 7.01mH$